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Loretta S. Garcia

February 10, 2006

CUSTOMER NO. 36257

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Eliyahou HARARI et al.

Title: Flash EEPROM System

Application No.: 09/064,250 Filing Date: April 22, 1998

Examiner: Vu Anh LE Group Art Unit: 2824

Docket No.: SNDK.006USM Conf. No.: 5711

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, Applicant(s) call(s) the documents listed on the enclosed Form PTO-1449 to the Examiner's attention in this patent application.

- According to 37 C.F.R. 1.98(2)(ii), copies of the U.S. Patents and U.S. Published Patent Applications documents are not required and are therefore not enclosed. Copies of the listed foreign patent documents or Other Art are enclosed.

Attorney Docket No.: SNDK.006USM

Application No.: 09/064,250

Citation of these documents shall not be construed as (1) an admission that the documents are prior art with respect to the invention or inventions claimed in this application, (2) a representation that a search has been made (other than as indicated by any cited document), or (3) an admission that the cited information is, or is considered to be, material to patentability as defined in § 1.56(b).

This information disclosure statement is submitted under 37 C.F.R. § 1.97(b) and consequently no fee should be required. The Commissioner is authorized, however, to charge any fee that may be required, or to credit any overpayment, against Deposit Account No. 502664. This form is being submitted in duplicate.

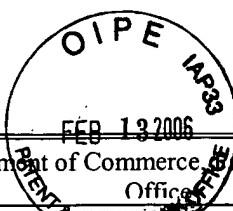
Respectfully submitted,

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U.S. Department of Commerce Patent and Trademark Office		Atty. Docket No.	Application No.
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		SNDK.006USM	09/064,250
		Applicant(s)	Conf. No.
(Use several sheets if necessary)		HARARI et al.	5711
(Form PTO-1449)		Filing Date	Group
		April 22, 1998	2824

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*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
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	4,149,270	04-1979	Cricchi et al.			
	4,181,980	01-1980	McCoy			
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	4,272,830	06-1981	Moench			
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	4,393,475	07-1983	Kitagawa et al.			
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	4,475,194	10-1984	LaValee et al.			

Foreign Patent Documents

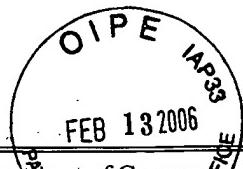
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	Document	Date	Country	Class	Subclass	Yes	No
	DE3637682	1987	Germany			abstract	
	DE3831538	1989	Germany			abstract	
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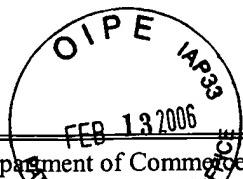
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
		JP5931158	1984	Japan			X	
		JP62188100	1987	Japan			abstract	
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	Furuyama et al., "An Experimental 2-BIT/CELL Storage Dram for Macro Cell or Memory-on-Logic Application", IEEE Custom Integrated Circuits Conference, May 1988, pp. 4.4.1-4.4.4.
	Gee et al., "An Enhanced 16K E2PROM", IEEE Journal of Solid-State Circuits, Vol. SC-17, No.5, October 1982, pp. 828-832.
	Horiguchi et al. "An Experimental Large-Capacity Semiconductor File Memory Using 16-LEVEL/CELL storage", IEEE Journal of Solid-State Circuits, Vol. 23, Feb.1998, No.1, pp. 27-33.

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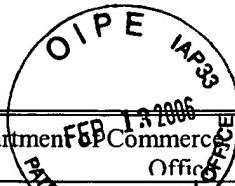
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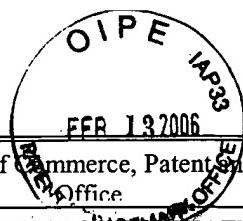
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	Tanaka et al., "A 4-Mbit NAND-EEPROM with Tight Programmed Vt Distribution", 1990 Symposium on VLSI Circuits, Digest of Technical Papers, pp. 105-106.

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